



PATENT  
790001-2004

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9/9/03  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Hiroyuki YANO et al.  
Serial No. : 09/870,085  
Filed : May 30, 2001  
For : **MANUFACTURING METHOD OF SEMICONDUCTOR  
DEVICE**  
Examiner : Thanhha Pham  
Group Art Unit : 2813

745 Fifth Avenue  
New York, New York 10151

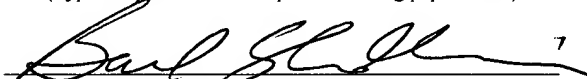
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**PRELIMINARY AMENDMENT**

Mail Stop RCE  
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P.O. Box 1450  
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Sir:

Prior to continued examination of the above-identified patent application,  
and in response to the Final Action mailed on March 24, 2003, please amend the  
application as follows.